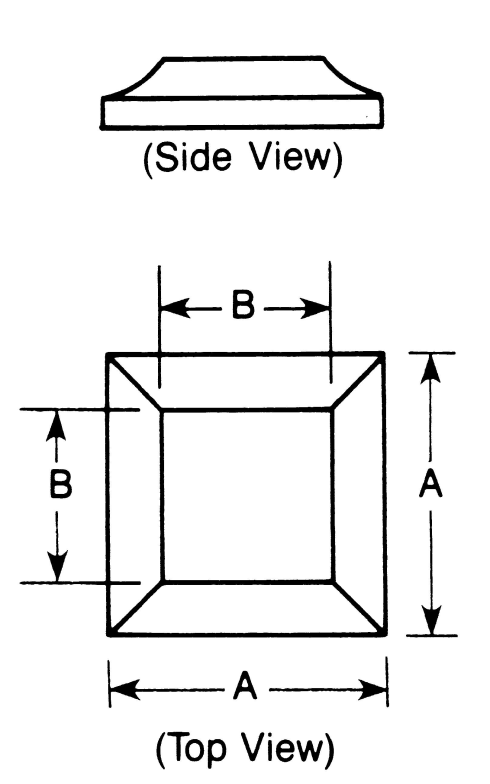
Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**ANODE**



**(SIDE VIEW)**

**CHIP BACK IS CATHODE**

**Top Material: Al**

**Backside Material: TiNiAg**

**Bond Pad Size: .025” X .025”**

**Backside Potential: Cathode**

**APPROVED BY: DK DIE SIZE .047” X .047” DATE: 10/18/21**

**MFG: ON SEMI/MOTOROLA THICKNESS .015” P/N: 1N4005**

**DG 10.1.2**

#### Rev B, 7/19/02